**Requirements:**

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| Requirements ID | Description |
| H\_01 | To develop a simple and user friendly system to compute all logic gates functions |
| H\_02 | To perform the operation of NOT gate |
| H\_02\_L\_01 | For both two input and three input values |
| H\_03 | To perform operation of AND gate. |
| H\_03\_L\_01 | For both two and three input values |
| H\_04 | To perform the operation of OR gate |
| H\_04\_L\_01 | For both two input and three input values |
| H\_05 | To perform operation of NAND gate. |
| H\_05\_L\_01 | For both two and three input values |
| H\_06 | To perform the operation of NOR gate |
| H\_06\_L\_01 | For both two input and three input values |
| H\_07 | To perform operation of XOR gate. |
| H\_07\_L\_01 | For both two and three input values |
| H\_08 | To perform operation of XNOR gate. |
| H\_08\_L\_01 | For both two and three input values |
| H\_09 | To provide user with list of operations available, including exiting the application function. |